

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant :** Yoshitsugu MORITA et al.

**App. Serial No.:** 10/599,041

**Group Art Unit:** 1791

**Atty. No. :** DC10032PCT (71,051-071)

**Examiner:** Huda, Saeed M.

**Filed :** July 10, 2007

**Title :** SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THEREOF

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**RESPONSE**

**MAIL STOP AMENDMENT**

**Commissioner for Patents**

P.O. Box 1450

Alexandria, Virginia 22313-1450

Dear Sir:

In response to the Office Action mailed on June 2, 2010, please consider the accompanying remarks. Although no amendments are currently made to the claims, a complete listing of all claims pending in the application is included for the Examiner's convenience. This Response is timely filed such that it is believed that no fees are presently due.